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U.S. PATENT DOCUMENTS									
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TM	A 1	JP	7-73034		NEC Corp.	3/17/1995		X+
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		OTHER - NON PATENT LITERATURE DOCUMENTS		
Examiner 5		Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item	Trans	slation
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^{*} A concise statement of relevance is being submitted in lieu of a translation. 37 CFR 1.98(a)(3).

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FOREIGN PATENT DOCUMENTS										
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	A 1	JP	7-73034		NEC Corp.	3/17/1995	X ⁺			

		OTHER - NON PATENT LITERATURE DOCUMENTS					
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TM	A2	KURIAN et al., Classification and performance evaluation of instruction buffering techniques, website, May 3, 2003, pp. 153, 157, IEEE Xplore	EI	ÆD			
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		publisher, city and/or country where published. Intel Architecture Software Developer's Manual-Instructions Formats and Encoding						
TM.		Appendix B; Order Number 243191; Intel; 1997; Vol. 2; p. B1-B10.						
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